## PATENT APPLICATION

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re	Application Of	)		
		)	Group Art Unit:	2827
Ken Takeuchi et al.		)		
		)	Examiner: H. H.	<b>o</b> ;
Serial	No.: 10/073,999	)		** **
		)		
Filed: February 14, 2002		)	Confirmation No. 9741	
E	Namualatila Caminan duatan Mamanu	)	A44-, DI4 No. 0017	01 00140
For:	Nonvolatile Semiconductor Memory	)	Atty. Dkt. No. 0017	01.00140
	Having Plural Data Storage Portions	)		
	For A Bit Line Connected To Memory	)		
	Cells	)		

U.S. Patent and Trademark Office Mail Stop <u>After Final</u> Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

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## **Amendment**

The paper is responsive to the final Office Action dated March 14, 2006. Applicants request entry of the following remarks into the file of the above-identified application. If any fees are required for this paper, please charge such fees to deposit account no. 19-0733.

Listing of Claims begins on page 2 of this paper.

Remarks begin on page 7 of this paper.

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